

In the United States Patent and Trademark Office

In re Patent Application:

Inventor: Ratson MORAD et al.

Serial No.: Unknown. (Divisional of SN 09/680,984.)

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Title: System Architecture of Semiconductor  
Manufacturing Equipment

Prior Examiner: Hassanzadeh, P.

Prior Group Art Unit: 1763

Attorney docket: AM 5296.D1

Mail Stop PATENT APPLICATION  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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**PRELIMINARY AMENDMENT**

Please amend the concurrently filed Divisional patent application as follows: